

FIG. 1

X AND Y DATA BUS IMPLEMENTATION

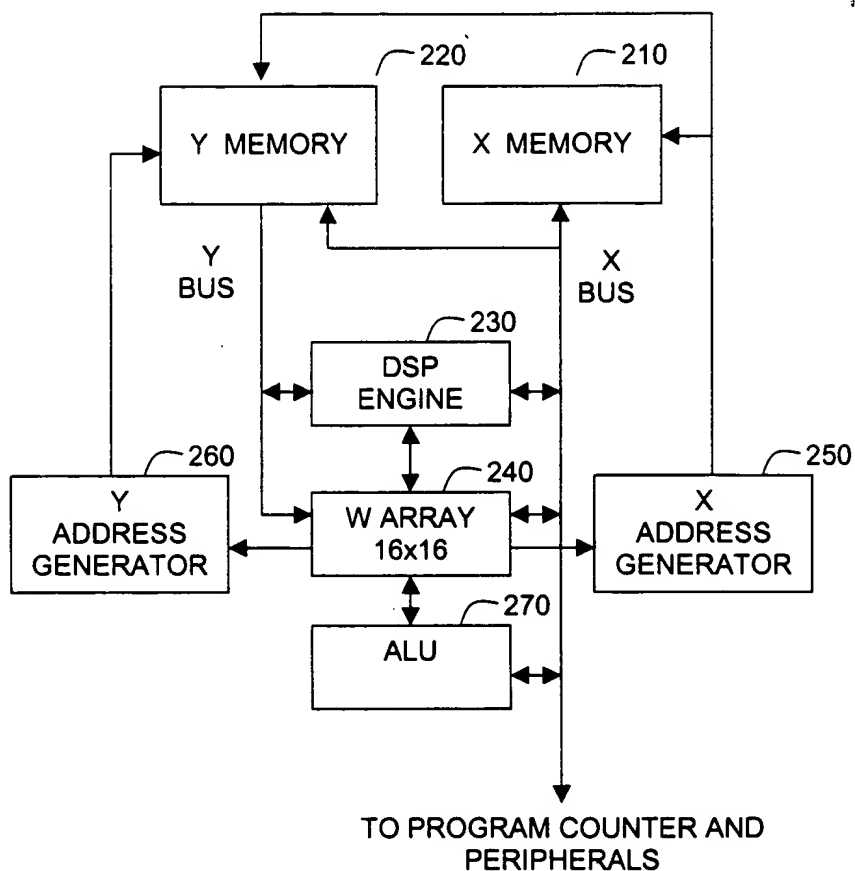


FIG. 2

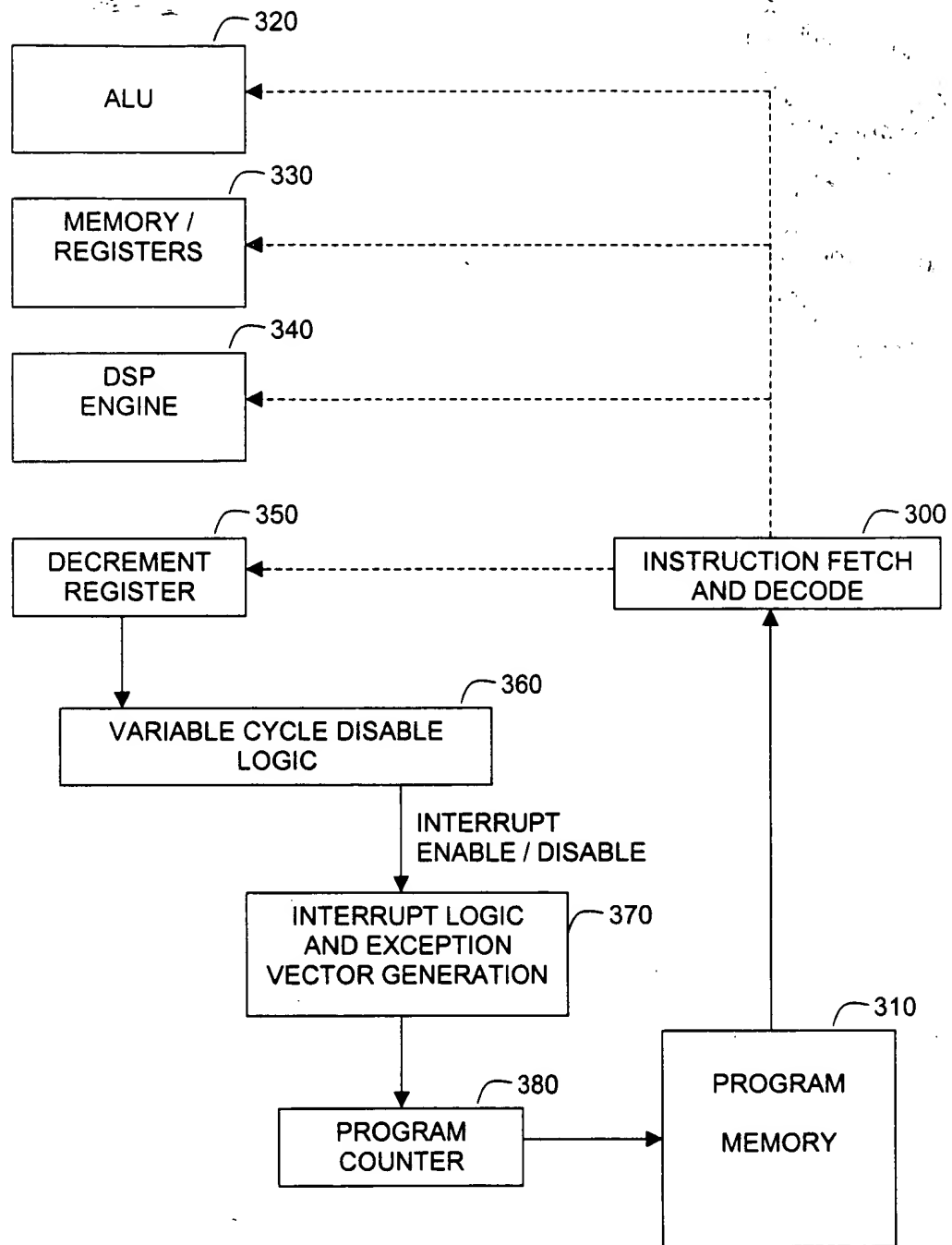


FIG. 3

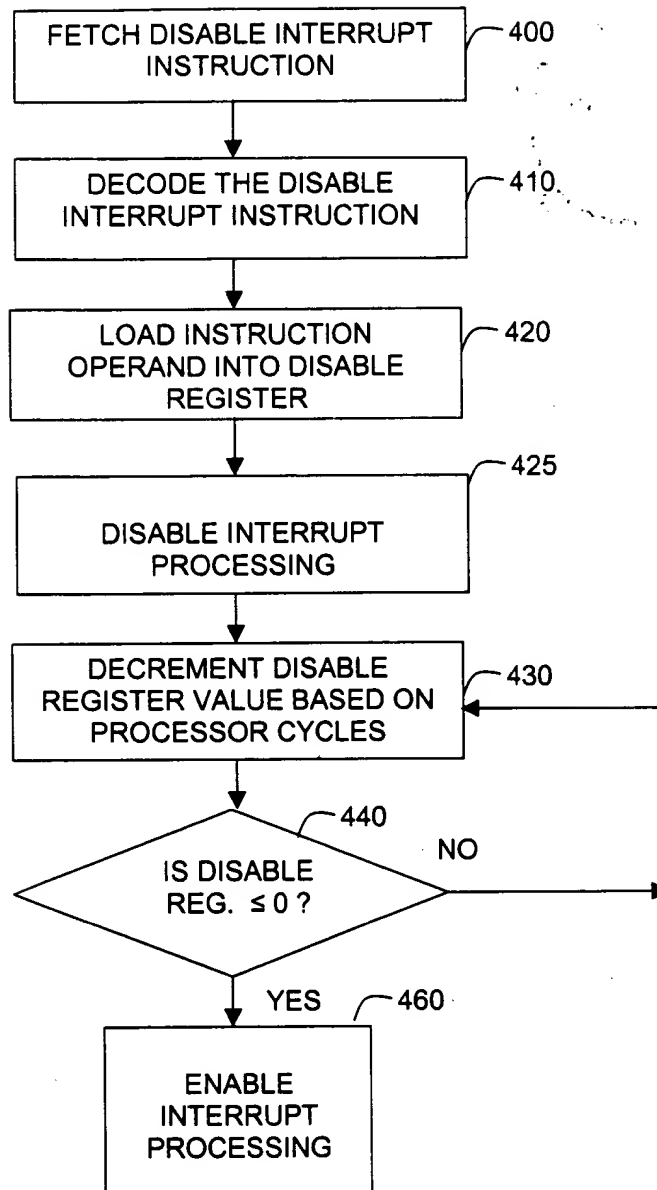


FIG. 4